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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>				<i>Complete If Known</i>	
				Application Number	10/015,194
				Filing Date	November-20, 2001
				First Named Inventor	Jam s S. Koford
				Group Art Unit	2184
				Examiner Name	
Sheet	1	of	1	Attorney Docket No.	/ 01-390

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials	Cite No.	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where	T
		<del>Simplify Programmable Logic IP Integration - Author(s) - Cravotta, Robert</del>	
		<del>Altera Casts Spotlight On IP Integration Tool - Author(s) - Souza, Crista</del>	

Examiner signature	<b>ALBERT W. PALADAN</b> <i>Albert W. Paladan</i> <b>PATENT EXAMINER</b>	Date considered	2-22-05
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1/25/05

# STATEMENT BY APPLICANT

Filing Date	NOVEMBER-20, 2001
First Named Inventor	James S. Koford
Group Art Unit	2184 2125
Examiner Name	
Attorney Docket No.	/ 01-390

Sheet 1 of 1

## U.S. PATENT DOCUMENTS

Examiner Initials	Cite No.	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code (if known)			
Amf		5,553,002		Dangelo et al.	09-03-1996	
Amf		6,678,875		Pajak et al.	01-13-2004	
Amf		6,721,922		Walters et al.	04-13-2004	
Amf		6,742,165		Lev et al.	05-25-2004	
Amf		2003009724		Koford et al.	05-22-2003	
Amf		2004002511		Chris Hamlin	02-05-2004	
Amf		6,634,008		Dole	10-14-2003	
Amf		6,704,917		Curran et al.	03-09-2004	
Amf		6,269,467		Cheung; Cyrus C. (Hercules,	06-10-2003	
Amf		6,253,358		Shuji Takahashi	06-26-2001	

## OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

Examiner Initials	Cite No.	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, caalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where	T
Amf		"A rule based VLSI process flow validation system with macroscopic process simulation", IEEE Transactions on semiconductor manufacturing, vol. 3, no. 4, November 1990, oages 239-246 - Author(s) - Funakoshi et al.	

Examiner signature	ALBERT W. PALADINI PATENT EXAMINER	Date considered	2-22-05
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# INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Complete If Known

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Filing Date	November 20, 2001
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Group Art Unit	2184
Examiner Name	
Attorney Docket No.	/01-390

Sheet 1 of 1

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Examiner Initials	Cite No.	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Word Code (if known)			
QW		5,222,030		Dangelo et al.	06-22-1993	
QW		6,574,778		Chang et al.	06-03-2003	

## OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

Examiner Initials	Cite No.	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where	T
QW		"Rapid Chip Management System"; Filed November 20, 2002; US Serial #10/301,182 - Author(s) - DANIEL M. WEED	

Examiner signature	ALBERT W. PALADIN PATENT EXAMINER	Date considered	2-22-05
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